
Official LG G4 VS986 USA Verizon Stock Rom



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Check out This Awesome LG G4 VS986 Stock Firmware from Firmwarez. There are Several Different Versions of Firmware Downloaded Below. An RFID tag system is known as an identification system in which information about identification of an object is stored as a RFID tag and identification information is read from the RFID tag. As shown in FIG. 21, the RFID tag system includes a reader/writer 101, a RFID tag 102 and a memory 103. The RFID tag 102 includes a memory 104 in which identification information about the object is stored and an antenna 105 for sending and receiving an electromagnetic wave. The

RFID tag 102 can be attached to an object by being soldered on or welded to the object. The reader/writer 101 includes a transmitter/receiver 107, a memory 109 in which various types of information are stored and a power supply circuit 110 which supplies electric power. The reader/writer 101 can read information stored in the memory 104 of the RFID tag 102 and transmit

the information to the memory 109 of the reader/writer 101. The reader/writer 101 can also write information into the memory 104 of the RFID tag 102. The memory 109 of the reader/writer 101 stores various types of information. The power supply circuit 110 generates electric power by receiving an external electric power source. The memory 109 is a non-volatile memory which does not lose stored information even if the electric power is cut off. The memory 104 of the RFID tag 102 stores identification information. When the identification information stored in the memory 104 matches the identification information stored in the memory 109 of the reader/writer 101, the antenna 105 sends an electromagnetic wave to the memory 104. In response to this, the memory 104 transmits the identification information stored therein to the reader/writer 101 through the antenna 105. The memory 104 is an SRAM (Static Random Access Memory) which can store information for a long time without any electric power. The reader/writer 101 includes an interface circuit 111, a program memory 112, a control circuit 113, a random access memory 114, a central processing unit 115, a bus control circuit 116, a power supply circuit 117 and a built-in antenna 118. The interface circuit 111 receives and transmits data to and from the memory 109 of the reader/writer 101 through the bus control circuit 116. The program memory 112 stores a program for implementing an operation of the reader/writer 101. The control circuit 113 controls the operations of the 82157476af

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